

Electronic Acknowledgement Receipt

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Application Number:	10777261
Confirmation Number:	5407
Title of Invention:	Apparatus and method for performing static timing analysis of an integrated circuit design using dummy edge modeling
First Named Inventor:	Craig Marshall Darsow
Customer Number:	46296
Filer:	Derek P. Martin
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
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1		OA1_response_amendment.pdf	83066	yes	9
	Multipart Description				
	Doc Desc		Start	End	
	Amendment - After Non-Final Rejection		1	6	
	Applicant Arguments/Remarks Made in an Amendment		7	9	
Warnings:					
Information:					
2	Terminal Disclaimer Filed	Terminal_disclaimer.pdf	212730	no	1
Warnings:					
Information:					
3	Fee Worksheet (PTO-875)	fee-info.pdf	8188	no	2
Warnings:					
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Total Files Size (in bytes):			303984		
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